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23125 7590 05/02/2005

FREESCALE SEMICONDUCTOR, INC.
LAW DEPARTMENT
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AUSTIN, TX 78729



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Pat Thomas

(Depositor's name)

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(Date)

5-12-05

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/694,146	10/27/2003	Bennett A. Joiner	SC12810TK	3275

TITLE OF INVENTION: ELECTROMAGNETIC NOISE SHIELDING IN SEMICONDUCTOR PACKAGES USING CAGED INTERCONNECT STRUCTURES

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/02/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
IM, JUNGHWAM	2811	257-690000			

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1 Robert L. King

2 Michael P. Noonan

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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FREESCALE SEMICONDUCTOR, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

AUSTIN, TEXAS

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Authorized Signature

Robert L. King

Date

May 11, 2005

Typed or printed name

Robert L. King

Registration No.

30,185

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PTOL-85 (Rev. 12/04) Approved for use through 04/30/2007.

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PAGE 2/3 * RCVD AT 5/12/2005 10:52:35 AM [Eastern Daylight Time] * SVR:USPTO-EXRF-2/2 * DNIS:7464000 * CSID:512 996 6853 * DURATION (mm:ss):01:52

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FROM: Pat Thomas for Robert King (512) 996-6839
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TOTAL NUMBER OF PAGES 3 (including this page)

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Docket No.: SC12810TK
Applicant: Bennett A. Joiner et al.
Serial No.: 10/694,146
Art Unit: 2811
Filed: October 27, 2003
Class: 257-690000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
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